Questions

1. What is the state of the condition code register (CCR) after the first ldaa instruction? Why?
   After the first ldaa instruction, the state of the CCR is %11011000. This is because after a reset, the S, X, and I bits are always set, and the other bits are not determined; then the ldaa instruction affects the Negative, Overflow, and Zero bits, the CCR should be %11?1 100? where ? could be 1 or 0.

2. How many times does the program loop through the code at the section labeled loop?
   The program loop through the code at the section labeled loop 10 times.

3. What is the final value in the B accumulator after the loop is finished?
   After the loop is finished, B accumulator contains $64 = 100

4. What finally causes the code to break out of the loop?
   Once the accumulator A becomes zero, it breaks the loop.

5. What were the contents of the D register and the CCR after the tab instruction executes? Why?
   D register has a value of $FEFE after the TAB instruction since it copies of A into B and D is the concatenation of A and B. The CCR after TAB instruction is %11111000.

6. What were the contents of the B register before and after the eorb instruction execution?
   Before the EORB instruction, B is $BE, and after the instruction, it is $41.