

Lab 3 Prelab

Read through the Quartus II Simulation with Verilog tutorial and answer the questions below. You are not required to use the lab machines; the purpose of this prelab is to get acquainted with some of the simulation features in Quartus II using Altera's DE-2 Board. However it may be helpful to you to run through the examples provided in the tutorials to make the lab exercises go more smoothly.

1. What tool of the Quartus II software is used to simulate circuit behavior?
2. Which simulation takes longer: a timing simulation, or a functional simulation? Why?
3. If you are having difficulty finding a desired signal because the Quartus II compiler modifies the original names, what can you do to force the compiler to keep the original names?