

## Lab 6 Prelab

Read through the Using Library Modules in Verilog tutorial and answer the questions below. You are not required to use the lab machines; the purpose of this prelab is to get acquainted LPMs in Quartus II using Altera's DE-2 Board. However it may be helpful to you to run through the examples provided in the tutorials to make the lab exercises go more smoothly.

1. Where would you go in the Quartus II software to see a list of available LPMs?
2. According to the tutorial, when should you consider using an LPM?
3. Where can you find templates on how an LPM can be instantiated in a hardware description language?