Am I in the right class?

- EE2304 Recitation
  - You scheduled for a EE2304 lab... then you need to be here (every week)
Administrative...

- Rade Trimceski (M.S. Student)
  - Office:
    - 719 EERC
    - phone: 487-0021 (rarely answer the phone)
    - email: rtrimces (from any on-campus unix machine)
    - rtrimces@mtu.edu (best way to contact me)

Administrative continued...

- Quiz every week
  - just to motivate you to come to class
- I will go over things you need to do & know for the upcoming lab
  - I designed the ‘Digital Logic’ labs, so it’s worth your time to come and pay attention
- I will discuss (answer?) some of the prelab questions
What are we going to do?

- EE2304 covers Digital Logic & DSP First
- Digital Logic labs:
  - Discrete logic design (two labs)
  - Altera UP2 development board (tutorial+lab)
- DSP labs:
  - Matlab, Matlab, and then some more Matlab...

What is discrete logic?
UP2 Board...

- We will use Max+PlusII® to design & program
- One can also use Quartus®...

What is Matlab?

- Most powerful tool that Electrical (and some Computer) Engineers use...
- You will learn to like it and use it...
Outline of the semester

- [link](http://www.ece.mtu.edu/labs/EElabs/EE2304/)
- Please read the “Course Policies” page from the link above.
- I will post a pdf of the slides I use during the discussion session under Lecture Notes
- ALL LAB SECTIONS MEET IN EERC622