Objectives

The purpose of this lab is to become familiar with the 68HC11 on chip timer/counter system. In part 1 of this lab, you will program the Real Time Interrupt system. In part 2, you will learn to use the compare register(s) to generate a waveform of known duty cycle and in part 3 use that knowledge to implement a Pulse With Modulator.

Turn-In Requirements:

1. Pages 2, 3, 4 and answers to the 4 questions as specified in the lab policies handout.

References


Equipment for this lab:

- 68HC11-trainer kit, to include 68HC11 EVBU and prototyping strips.
- IBM compatible PC to connect to the trainer kit via an RS-232 serial cable.
- Agilent 54621D oscilloscope.
Laboratory Exercise

Name: _______________________________ Signature: ___________________________

Notes

- Make sure your development EVBU board is connected to power (green LED on board ON) and the serial port of the EVBU is connected to the serial port of your development PC containing the AXIDE software, configured to the correct port at the correct baud rate, etc.

- The lab TA has a copy of s19 files for Parts 1, 2 and 3. TA will demonstrate the waveforms to you at the beginning of the class.

Part 1

In this part, you will program the Real Time Interrupt (RTI) system to generate interrupts at a known rate, and use the interrupts to generate a waveform of known frequency. Please read pages 10-11 and 10-12 of the 68HC11 reference manual for a detailed description of the RTI system. The bits “RTR1” and “RTR0” in register “PACTL” determine the pre-scalar for the clock input, and the bit “RTII” in register “TMSK2” enables the RTI interrupts. The bit “RTIF” in register “TFLG2” indicates that a RTI has occurred. Note that the clock oscillator frequency of the Axiom board is 8.0MHz.

Write a program to generate real time interrupts at 32.7 mS intervals and create a square wave of 15.25Hz, 50% duty cycle using the interrupt service routine at PORT D. Use the Agilent 54621D oscilloscope to monitor the waveform.

Fill in the values of the “PACTL” and “TMSK2” registers below. Disable all the other interrupts except RTI. In addition to enabling Real Time Interrupts, you also need to globally enable all the maskable interrupts.

PACTL $1026

<table>
<thead>
<tr>
<th>DDAR7</th>
<th>PAEN</th>
<th>PAMOD</th>
<th>PEDGE</th>
<th>0</th>
<th>0</th>
<th>RTR1</th>
<th>RTR0</th>
</tr>
</thead>
<tbody>
<tr>
<td>X</td>
<td>X</td>
<td>X</td>
<td>X</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

TMSK2  $1024

<table>
<thead>
<tr>
<th>TOI</th>
<th>RTII</th>
<th>PAOVI</th>
<th>PAII</th>
<th>0</th>
<th>0</th>
<th>PR1</th>
<th>PR0</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0</td>
<td>X</td>
<td>X</td>
<td>X</td>
</tr>
</tbody>
</table>

Please note that the interrupt vectors are re-mapped in the Axiom board to lower RAM locations. You can find these locations in page 23 of the “CME11E9-EVBU development board” documentation under “Buffalo Monitor Interrupt Jump Table.” For example, RTI is relocated at $00EB - $00ED, and you should put a “jmp int_RTI” at this location. (“int_RTI” is the name of the Real Time Interrupt service routine.)
Demonstrate to the TA that your program is working.

T. A. Initials: ______________________

**Part 2**

In this part you will use the output-compare function of 68HC11 to generate waveforms of a known duty cycle. Please read pages 10-27 to 10-36 of 68HC11 reference manual for a detailed description of the output-compare function.

An output compare is generated when the free running counter of 68HC11 matches the value of “TOCx” counter. This signal can be used to generate an interrupt if the corresponding interrupt enable bit in the register “TMSK1” is set. The “OMx” and “OLx” bits in register “TCTL1” determine the state of output pin “Ocx” when the free running counter matches the “TOCx” counter. Refer to the table in page 10-31 of the 68HC11 reference manual for details.

Program the output-compare channel 3 (OC3) to generate a waveform of the following characteristics at pin PA5/OC3.

- **Period** = 2.5ms
- **High** = 1.5ms
- **Low** = 1.0ms
- **Duty cycle** = High/Period = 60%

You need to program “TMSK1” and “TCTL1” registers with appropriate values to generate the waveform. Enable Output Compare 3 Interrupt in the “TMSK1” register, and disable all the other interrupt enable bits in the register. Program the appropriate bits in the “TCTL1” register to generate the required waveform at PA5/OC3 pin.

<table>
<thead>
<tr>
<th>TCTL1 $1020</th>
</tr>
</thead>
<tbody>
<tr>
<td>OM2</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TMSK1 $1022</th>
</tr>
</thead>
<tbody>
<tr>
<td>OC1I</td>
</tr>
</tbody>
</table>

The following method to generate the waveform is suggested.

- Allocate 3 memory locations for High, Low and the Period.
- Initialize these locations to appropriate values.
- Enable Output Compare 3 Interrupts.
• Within the interrupt service routine, change the value of the ‘TOC3’ counter, so that the next compare occurs at the appropriate time.

T. A. Initials:______________________

Part3

In this part, you generate a PWM signal by changing the duty cycle of the waveform generated in part 2. Use the Real Time Interrupt generated in part 1 to modify the duty cycle of the waveform generated in part 2 from 5% to 95%. That is, modify the values of “High” and “low” inside the Real Time Interrupt service routine. When the duty cycle reaches 95%, then reset it back to 5%. The Period of the waveform doesn’t change. Note that both interrupt service routines are accessing the same data (High, Low, Period) and appropriate precautions should be taken when modifying shared data. Make sure the duty cycle increases from 5% to 95% (not decreases from 95% to 5%).

T. A. Initials:______________________

Questions

1. What are maskable and unmaskable interrupts? Give two examples of each.
2. Where are the vector addresses for the following interrupts.
   (a) Real Time Interrupt
   (b) Timer Output Compare 2
   (c) Pulse Accumulator Overflow
   (d) Software Interrupts

   list them according to increasing priority. That is, the lowest priority interrupt first and the highest priority interrupt last.

3. What values are necessary (High, Low, Period) to generate a 10% duty cycle, 1000Hz waveform in Part 2.
4. Describe one important use of Pulse Width Modulation.